

(19)



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11) Publication number: 62126661 A

(43) Date of publication of application: 08.06.87

(51) Int. Cl.

H01L 25/04
H01L 21/60

(21) Application number: 60267712

(71) Applicant: NEC CORP

(22) Date of filing: 27.11.85

(72) Inventor: SAKATA HIROMI

(54) HYBRID INTEGRATED CIRCUIT DEVICE

(57) Abstract:

PURPOSE: To obtain a high-density, compact hybrid integrated circuit device, by mounting semiconductor pellets not in a planar arrangement but in a stacked state in two layers through resin.

CONSTITUTION: On an insulating substrate 1, on which a wiring conductor 2 is formed, a semiconductor pellet 4 is mounted with a bonding resin 3a. The electrode of the semiconductor pellet 4a and the wiring conductor 2 are bonded with an Au thin wire 5a. Then the semiconductor pellet 4a and the Au thin wire 5a are coated with a coating resin 6a. Thereafter, another semiconductor pellet 4b is mounted on the coating resin 6a, which is coated on the previously provided semiconductor pellet 4a with a bonding resin 3b. Then the pellet is connected with an Au thin wire 5b by the similar way as before. Thereafter, the entire body is coated with a coating resin 6b, and a hybrid integrated circuit device is completed.

COPYRIGHT: (C)1987,JPO&Japio

